MAY 1 1 2001

BADEN

Examiner

Signature

PTO/S8/08A (10-96)
Approved for use through 10/31/99. OMB 0651-0031
Patent and Trademark Office: U.S. DEPARTMENT OF COMMERCE

Patent and Trademark Office: U.S. DEPARTMENT OF COMMERCE
Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it contains a valid OMB control number.

Complete if Known Substitute for form 1449B/PTO 09/665,884 Application Number INFORMATION DISCLOSURE **Filing Date** September 20, 2000 STATEMENT BY APPLICANT **First Named Inventor** Christopher H. Dick Group Art Unit (use as many sheets as necessary) **Examiner Name** Not Yet Known Sheet of **Attorney Docket Number** X-501-1P US

<u>~</u>			
		OTHER PRIOR ART NON PATENT LITERATURE DOCUMENTS	
Examiner Initials	Cite No '	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
₽/ _{\(\text{\tin}\exitt{\text{\tin}\text{\texi\text{\texi}\text{\text{\texi}\text{\text{\text{\text{\text{\text{\text{\text{\texi}\text{\text{\texit{\ti}\tinttitt{\texi}}\\tinttitex{\text{\texi}}}\text{\ti}		Hwang, Kai, "Computer Arithmetic Principles, Architecture, and Design"; Copyright 1979 by John Wiley & Sons, Inc.; pages 152-155.	
		Marvin E. Frerking, "Digital Signal Processing in Communication Systems", published by Van Nostrand Reinhold, New York, 1994, pp. 124-128 and 171-174.	
		John R. Deller, Jr. et al., "Discrete Time Processing of Speech Signals", published by Macmillan Publishing Company, Maxwell Macmillan International, New York, 1993, pp. 273-280.	
		"The Programmable Logic Data Book", 1999, available from Xilinx, Inc., 2100 Logic Drive, San Jose, California 95124, pp. 6-5 to 6-41.	
		L. Mintzer, "FIR Filters with Field-Programmable Gate Arrays", Journal of VLSI Signal Processing, No. 6, 1993, pp. 119-127.	Techn
		S. Mohanakrishnan et al., "Automatic Implementation of FIR Filters on Field Programmable Gate Arrays", IEEE Signal Processing Letters, Vol. 2, No. 3, March 1995, pp. 51-53.	plogy Cei
4		D. Patterson et al., "Computer Architecture: A Quantitative Approach", Morgan Kaufmann Publishers, Inc., California, 1990, pp. A8-A10.	ier 2800
*		C.H.Dick and F.J. Harris; "FPGA Signal Processing Using Sigma-Delta Modulation"; IEEE Signal Processing Magazine, January 2000	
		Atmel AT40K/05/10/20/40 Data Sheet, 1999	
		A. Peled and B. Liu, "A New Hardware Realization of Digital Filters" IEEE Trans. on Acoust., Speech, Signal Processing, Vol. 22, pp. 456-462, December 1974.	
		S.A. White, "Applications of Distributed Arithmetic to Digital Signal Processing" IEEE ASSP Magazine, Vol. 6(3), pp. 4-19, July 1989	
			=

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conforman and not considered. Include copy of this form with next communication to applicant.

Date

Considered

D2

Burden Hour Statement: This form is estimated to take 2.0 hours to complete. Time will vary depending upon the needs of the individual case. Any comments on the amount of time you are required to complete this form should be sent to the Chief Information Officer, Patent and Trademark Offic Washington, DC 20231. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Assistant Commissioner for Patents, Washington, DC 20231.

^{&#}x27;Unique citation designation number. 'Applicant is to place a check mark here if English language Translation is attached.

Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it contains a valid OMB control number.

Substitute	for form 1449B/PT	0		Complete if Known		
				Application Number	09/665,884	
			SCLOSURE	Filing Date	September 20, 2000	
STA	ATEMENT	BY A	APPLICANT	First Named Inventor	Christopher H. Dick	
				Group Art Unit	2631	
(use as many sheets as necessary)				Examiner Name	Not Yet Known	
Sheet	2	of	2	Attorney Docket Number	X-501-1P US	

	OTHER PRIOR ART NON PATENT LITERATURE DOCUMENTS							
Examiner Initials*	Cite No '	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T²					
	•	Application Note AN10, entitled "Delta Sigma A/D Conversion Technique Overview" January 1997, available from Crystal Semiconductor Corporation, P.O. Box 17847, Austin TX 78760, http://www.crystal.com, pp. 1-10.						
A,		Multidimensional autoregression Chapter 6, pp. 135-169						
2/3	C.H. Dick and F. Harris, "FIR Filtering with FPGAs Using Quadrature Sigma-Delta Modulation Encoding", 6th Int. Workshop on Field-Programmable Logic and Applications, FPL September 1996, Darmstadt, Germany							
Examin Signatu	er re	Date Considered Date Considered Date	<i>[</i>					

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformar and not considered. Include copy of this form with next communication to applicant.

Burden Hour Statement: This form is estimated to take 2.0 hours to complete. Time will vary depending upon the needs of the individual case. Any comments on the amount of time you are required to complete this form should be sent to the Chief Information Officer, Patent and Trademark Offi Washington, DC 20231. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Assistant Commissioner for Patents, Washington, DC 20231.

^{&#}x27;Unique citation designation number. ² Applicant is to place a check mark here if English language Translation is attached.